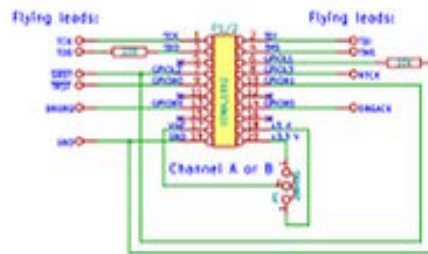


# Xilinx Digilent Usb Jtag Driver

Basic JTAG cable.  
Can be used for 0.9 V to 5 V systems.  
This isn't a circuit, is just a cable.



JP1 usage:  
Channel A: open  
Channel B:  
+5 V jumper in 2-3  
+3.3 V jumper in 1-2  
<3.3 V no jumper and connect  
the reference Vcc to pin 2

Author: Salvador E. Tropea  
For more information visit: <http://fpgaflow.com/MIU/>  
For license information read the LICENSE.txt file  
BOM - CSV  
File: JTAG\_base.csh  
Sheet: /  
Title: JTAG basic cable (flying leads) (Rev:01.01.1)  
Size: 44 | Date: 19 oct 2018 | Rev: 01/01  
Xilinx F.C.A. | 01/01

DOWNLOAD: <https://byltly.com/29oh2w>

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